

	Type	Hits	Search Text
1	BRS	21	((decrease OR minimize OR converge) ADJ3 (width OR range OR (two ADJ (number OR value)) OR period)) WITH ((locate OR determine) WITH value)
2	BRS	14	((decrease OR minimize OR converge) ADJ3 (width OR range OR (two ADJ (number OR value)))) WITH ((locate OR determine) WITH value)
3	BRS	1	((decrease OR minimize) ADJ (width OR range)) WITH ((locate OR determine) WITH value)
4	BRS	12	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((voltage ADJ range) SAME difference) AND ((voltage ADJ2 difference) SAME range)) AND ((narrow OR decrease OR reduce) ADJ2 range)
5	BRS	14	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((voltage ADJ range) SAME difference) AND ((voltage ADJ2 difference) SAME range)) AND ((narrow OR decrease) ADJ2 (range OR width))
6	BRS	13	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((voltage NEAR range) SAME difference) AND ((voltage NEAR difference) SAME range)) AND ((narrow OR decrease) NEAR (range OR width))
7	BRS	11	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((voltage NEAR range) SAME difference) AND ((voltage NEAR difference) SAME range)) AND ((narrow OR decrease) NEAR range)
8	BRS	10	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((voltage ADJ range) SAME difference) AND ((voltage ADJ2 difference) SAME range)) AND ((narrow OR decrease) ADJ2 range)

	Type	Hits	Search Text
9	BRS	103	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND (voltage ADJ range) AND (voltage ADJ2 difference)) AND ((narrow OR decrease) ADJ2 range)
10	BRS	11	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND (voltage ADJ range) AND (voltage ADJ2 difference)) AND (rank OR place OR evaluate OR order) AND ((narrow OR decrease) ADJ2 range)
11	BRS	146	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND (voltage ADJ range) AND (voltage ADJ2 difference)) AND (rank OR place OR evaluate OR order) AND (narrow OR decrease)
12	BRS	45	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND (voltage ADJ range) AND (voltage ADJ2 difference)) AND narrow
13	BRS	230	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND (voltage ADJ range) AND (voltage ADJ2 difference)) AND (rank OR place OR evaluate OR order)

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14	BRS	250	(semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND (voltage ADJ range) AND (voltage ADJ2 difference)
15	BRS	10	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC)) AND (test\$3 OR analyz\$3)) AND (differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality OR dual) ADJ (input OR terminal))
16	BRS	8239	((semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC)) AND (test\$3 OR analyz\$3)
17	BRS	20805	(semiconductor OR (device ADJ under ADJ test) OR DUT OR IC OR (integrated ADJ circuit)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC)
18	BRS	10	((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND ((generates OR selects OR outputs) ADJ3 (multiple OR plurality) ADJ3 voltages)
19	BRS	43	((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND ((multiple OR plurality) ADJ3 voltages)

	Type	Hits	Search Text
20	BRS	4	((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND (multiple ADJ voltages)
21	BRS	1	((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND (generates ADJ multiple ADJ voltages)
22	BRS	51	(((((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND semiconductor AND (IC OR (integrated ADJ circuit))) AND test\$3) AND range AND voltage
23	BRS	57	(((((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND semiconductor AND (IC OR (integrated ADJ circuit))) AND test\$3
24	BRS	76	((reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set) AND semiconductor AND (IC OR (integrated ADJ circuit))
25	BRS	191	(reference ADJ voltage ADJ generator) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter) OR DAC) AND select AND set
26	BRS	17	(LCD ADJ driver) AND LSI AND semiconductor AND (test\$3 OR analy\$4)

	Type	Hits	Search Text
27	BRS	109	(LCD ADJ driver) AND LSI
28	BRS	11	(LCD ADJ driver) AND LSI AND testing
29	BRS	228	(((((test\$3 OR analyz\$3) AND (IC OR (integrated ADJ circuit) OR device)).ti.) AND semiconductor) AND voltage AND range) AND (generat\$3 AND (reference OR base OR baseline OR (bse ADJ line) OR standard) AND compar\$4) AND (amplif\$7 OR modif\$7 OR adjust\$)
30	BRS	555	(((((test\$3 OR analyz\$3) AND (IC OR (integrated ADJ circuit) OR device)).ti.) AND semiconductor) AND voltage AND range
31	BRS	11498	(((((test\$3 OR analyz\$3) AND (IC OR (integrated ADJ circuit) OR device)).ti.) AND semiconductor
32	BRS	19	((reference ADJ voltage ADJ generator) AND (differen\$4 ADJ amplifier) AND comparator) AND semiconductor AND test\$3 AND range AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter))
33	BRS	497	(reference ADJ voltage ADJ generator) AND (differen\$4 ADJ amplifier) AND comparator
34	BRS	23	(((((differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality OR dual) ADJ (input OR terminal)) AND test\$3) AND semiconductor AND (IC OR (integrated ADJ circuit))) AND (generat\$3 AND (reference OR base OR baseline OR (bse ADJ line) OR standard) AND compar\$4) AND (amplif\$7 OR modif\$7 OR adjust\$)) AND range

	Type	Hits	Search Text
35	BRS	25	((differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality OR dual) ADJ (input OR terminal)) AND test\$3) AND semiconductor AND (IC OR (integrated ADJ circuit))) AND (generat\$3 AND (reference OR base OR baseline OR (bse ADJ line) OR standard) AND compar\$4) AND (amplif\$7 OR modif\$7 OR adjust\$)
36	BRS	31	((differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality OR dual) ADJ (input OR terminal)) AND test\$3) AND semiconductor AND (IC OR (integrated ADJ circuit))
37	BRS	626832	((differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality OR dual) ADJ (input OR terminal)) AND test\$3) AND semiconductor AND (IC OR (integrated ADJ circuit))
38	BRS	151	(differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality OR dual) ADJ (input OR terminal)) AND test\$3
39	BRS	4	(differen\$4 ADJ amplif\$ ADJ3 (two OR multiple OR plurality) ADJ terminal) AND test\$3
40	BRS	281	((voltage AND semiconductor AND (IC OR (integrated ADJ circuit) AND generat\$3 AND reference AND compar\$4) AND (amplif\$7 OR modif\$7 OR adjust\$)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter)) AND (differential ADJ amplif\$)) AND test\$3
41	BRS	595	(voltage AND semiconductor AND (IC OR (integrated ADJ circuit) AND generat\$3 AND reference AND compar\$4) AND (amplif\$7 OR modif\$7 OR adjust\$)) AND ((D/A ADJ converter) OR (digital-to-analog ADJ converter) OR (digital ADJ to ADJ analog ADJ converter)) AND (differential ADJ amplif\$)

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42	BRS	34536	voltage AND semiconductor AND (IC OR (integrated ADJ circuit) AND generat\$3 AND reference AND compar\$4) AND (amplif\$7 OR modif\$7 OR adjust\$)